ANALOG
PRODUCTS
MC33143
FACT SHEET

## 33143 SM ART DUAL HIGH-SIDE SWITCH ( $140 \mathrm{~m} \Omega \mathrm{R}_{\mathrm{DS}(\mathrm{on})}$ )

The 33143 is a dual high-side switch for use in controlling moderate current loads (1.0 A), such as small motors, relays, solenoids, and incandescent lamps.

Each output is enabled with a 5.0 V CM OS logic signal applied to a respective parallel input. When turned on, the outputs provide full supply (battery) voltage to the load.

An internal charge pump is incorporated to provide efficient gate enhancement of the internal high-side $N$-channel power output devices. The outputs are designed to provide drive current to low impedance solenoids. Features of the 33143 include individual output fault status reporting, interrupt output for system use, chip enable (sleep mode), and internal output independent over current and over temperature shutdown with automatic retry recovery. It also has over voltage protection with automatic recovery, which "globally" disables both outputs for the duration of the over voltage condition.

## CUSTOMER BENEFITS

- Low system cost, simplified circuitry, minimal boardspace
- Simple system design with direct interfacing to a microprocessor
- Easily used in stand-alone manual circuit modes (non-microprocessor applications)
- Simplified high-side switching of inductive loads with internally clamped outputs
- Applicable for high-side switching of capacitive, inductive, or incandescent loads

| Performance | Typical Values |
| :--- | :---: |
| Outputs | 2 |
| RDS(on) @ $25^{\circ} \mathrm{C}$ | $0.14 \Omega$ |
| Operating Voltage | $5.5-26 \mathrm{~V}$ |
| Peak Current | 3.0 A each output |
| ESD | $\pm 2000 \mathrm{~V}$ |
| Operating Temp |  |
| J unction Operating Temp | $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq 125^{\circ} \mathrm{C}$ |
|  | $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{J}} \leq 150^{\circ} \mathrm{C}$ |

## FEATURES

- Outputs clamped for sw itching inductive loads
- Very low operational bias currents $<2.0 \mathrm{~mA}$
- CM OS input logic compatible with 5.0 V logic levels
- Load dump robust ( 60 V transient at VPWR
- Sleep mode current $<25 \mu \mathrm{~A}$
- Switch outputs can be paralleled for higher currents
- Additional devices available for comparison in Analog Selector Guide SG1002/D

| Protection | Detect | Limiting | Shut Down | Auto Retry | Status Reporting |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Over Voltage | - |  | - |  | - |
| Over Current/SC | - | - | - | - | - |
| Over Temperature | - |  | - | - |  |
| Open Load | - |  |  |  | - |
| Short to GND | - |  | - | - |  |
| Short to VPW R | - |  | - | - |  |


| Ordering Information | Package | Ship Method | M otorola Part Number |
| :---: | :---: | :---: | :---: |
|  | 24 SOICW | $\begin{aligned} & \text { Rail } \\ & T / R \end{aligned}$ | $\begin{aligned} & * * 33143 D W \\ & \text { **33143DW R2 } \end{aligned}$ |
| Data Sheet Order Number |  |  | M C33143/D |
| Contact Sales for Evaluation Kit Availability |  |  |  |
| ** Prefix Index: |  |  |  |
| PC = Eng Samples; XC = In Qual; M C = Production |  |  |  |

## QUESTIONS

- Do you need to reduce system complexity for high-side switching of two loads using a microcontroller?
- Do you have only a little PC board space available for load control?
- Do you have to design a dual high-efficiency switch to control capacitive, incadescent, or inductive loads over a wide temperature range?
- Are you looking for an easy-to-design high-side solid state relay switch, capable of switching two loads?
- Do you require a "smart" switch with internal protection?


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## M motorola

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